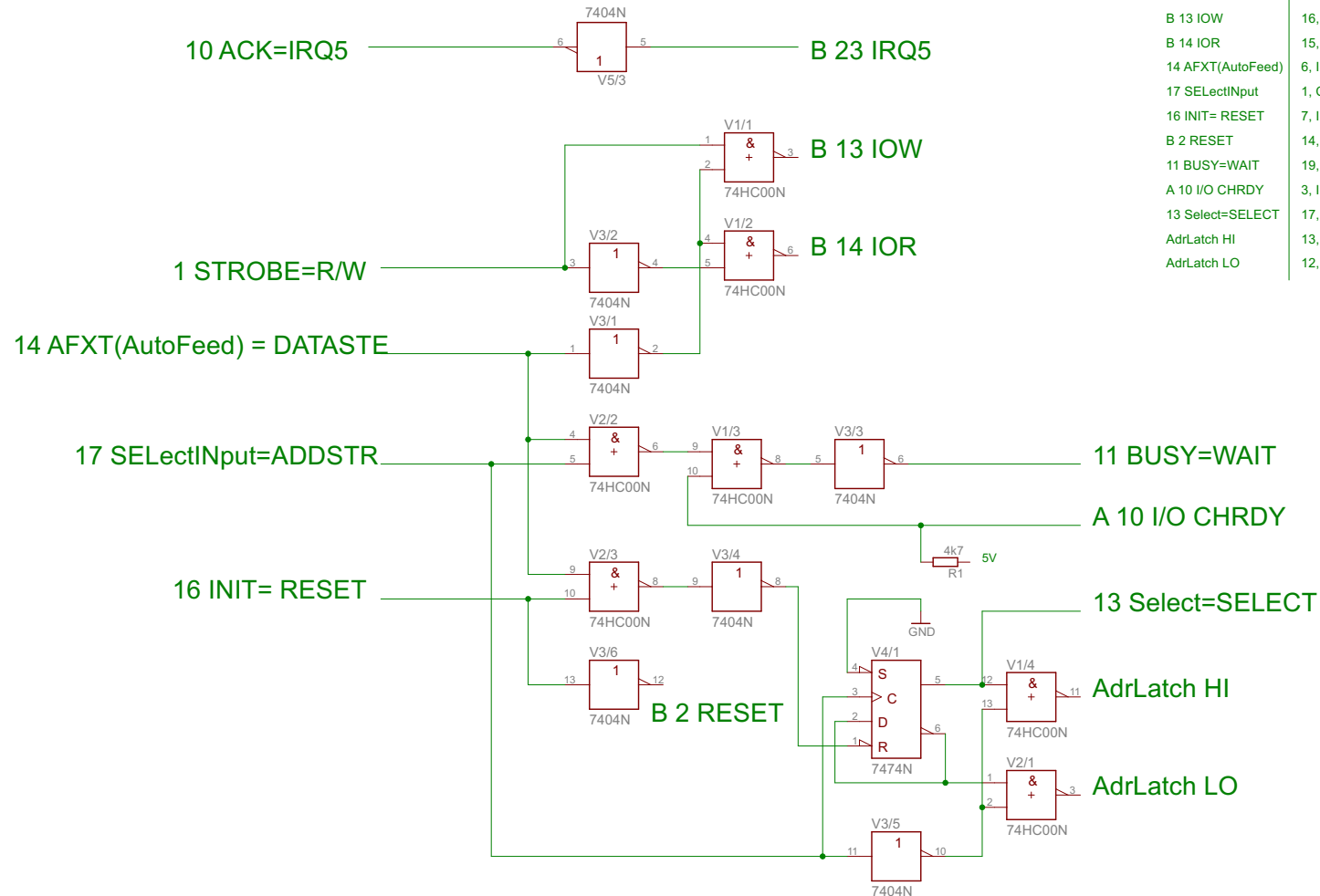


EPP Logik

Am besten im GAL das macht die Sache flexibel für Änderungen.

GAL Mapping	
Signal	PIN, I/O
10 ACK=IRQ5	18, O
B 23 IRQ5	8, I
1 STROBE=R/W	5, I
B 13 IOW	16, O
B 14 IOR	15, O
14 AFXT(AutoFeed)	6, I
17 SElectInput	1, CLK
16 INIT= RESET	7, I
B 2 RESET	14, O
11 BUSY=WAIT	19, O
A 10 I/O CHRDY	3, I
13 Select=SELECT	17, O
AdrLatch HI	13, O
AdrLatch LO	12, O



Wolfram Kaißer Esslingen
Revision 1.0

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